

# M88DR5RCD03

## Gen3 DDR5 RCD for RDIMM

### General Description

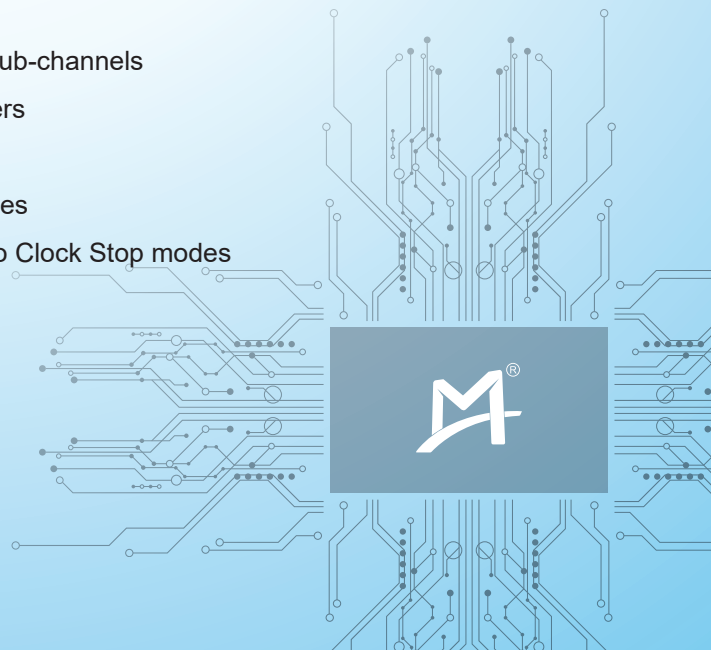
M88DR5RCD03 is a Gen3 DDR5 Registering Clock Driver (RCD03) at speed up to 6400MT/s. The RCD03 is compliant with the JEDEC DDR5RCD03 specification and can be used as a central buffer on RDIMM, to improve memory capacity, bandwidth, and latency for data center, cloud, and AI applications.

### Applications

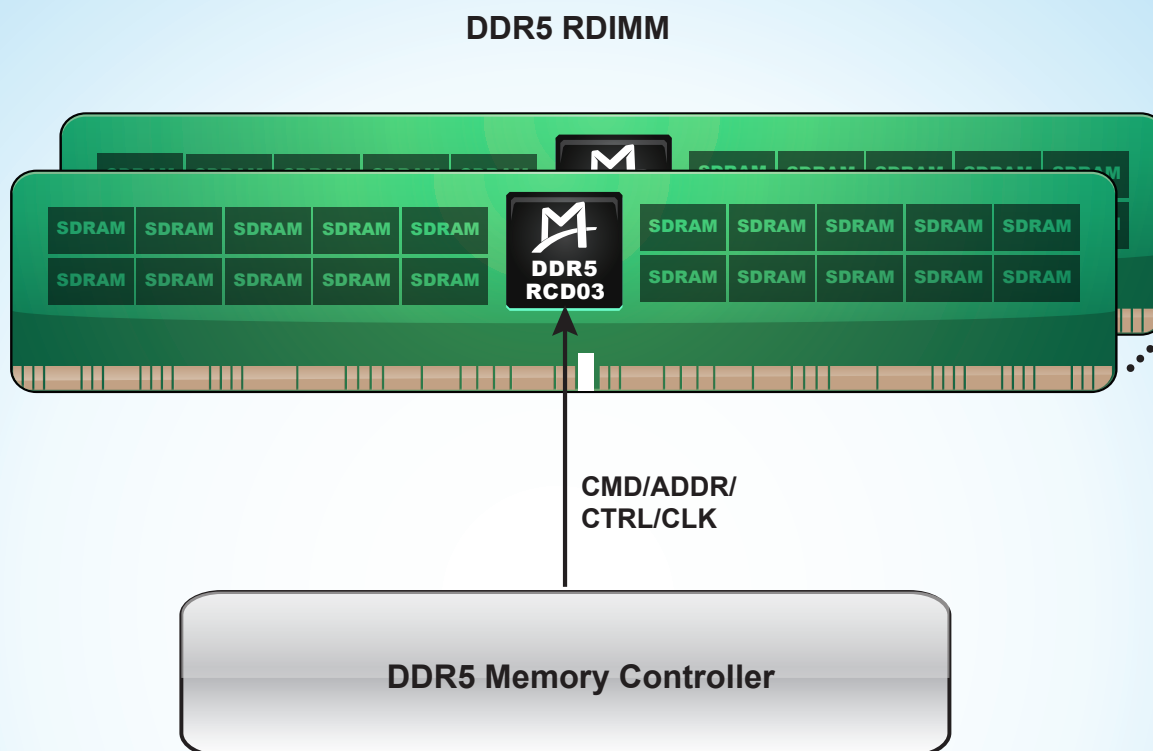
- DDR5 RDIMM

### Feature List

- Compliant with JEDEC DDR5RCD03 specification
- Speed up to 6400MT/s
- 1.1V VDD and 1.0V VDDIO voltages
- Two-channel 1:2 registering buffer for Command/Address inputs and control signals
- Integrated PLL clock driver distributing one differential clock pair to five
- Dual chip selects
- Parity checking across CA and DPAR separately on two sub-channels
- Output characteristics configurable through control registers
- CA, CS and DFE training modes
- Sideband bus interface supporting I<sup>2</sup>C and I3C Basic modes
- Power saving modes: PDE power down, Self refresh w/wo Clock Stop modes
- Green package: 240-ball FCBGA



## Application Diagram



Montage Technology Co., Ltd.

16/F, 181 Caobao Road, Montage Circle, Xuhui District, Shanghai 200233, China

Phone: +86-21-54679038

Fax: +86-21-54263132

Sales Contact: [globalsales@montage-tech.com](mailto:globalsales@montage-tech.com)

[www.montage-tech.com](http://www.montage-tech.com)



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